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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Embedded Processor with Watchdog Timer for Programmable Logic	
Application Number :		
Date :		
First Named Applica	ant: Andrew Crosland	
Confirmation Number:		
Attorney Docket Nur	mber: 15114-053510	

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Documents being submitted:	Files	\neg
us-request	15114-053510-usrequ.xml	
	us-request.dtd	
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us-fee-sheet	15114-053510-usfees.xml	
	us-fee-sheet.xsl	
	us-fee-sheet.dtd	
us-ids	15114-053510-usidst.xml	
	us-ids.dtd	
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application-body	5351abx.xml	
	application-body.dtd	
	Image1.tif	
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	mathml2.dtd	
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